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Term: L26 and port

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<u>L37</u>	L35 and port	12	<u>L37</u>
<u>L36</u>	L35 and path near tag\$	1	<u>L36</u>
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<u>L34</u>	L33 and packet	12	<u>L34</u>
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<u>L29</u>	mesh adj port and hash and load\$ near balanc\$	12	<u>L29</u>
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<u>L25</u>	L24 and IP	18	<u>L25</u>
<u>L24</u>	L22 and table	18	<u>L24</u>
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<u>L19</u>	L18 and load\$ near balanc\$	18	<u>L19</u>
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<u>L16</u>	L14 and hash\$	1	<u>L16</u>
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<u>L13</u>	L12 and switch\$ near mesh\$	8	<u>L13</u>
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<u>L11</u>	L9 and link near tag\$	0	<u>L11</u>
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<u>L1</u>	370/395.3.ccls.	295	<u>L1</u>

END OF SEARCH HISTORY

Patent Assignment Abstract of Title

Total Assignments: 1**Application #:** 10812267 **Filing Dt:** 03/29/2004**Patent #:** NONE**Issue Dt:****PCT #:** NONE**Publication #:** US20050213582**Pub Dt:** 09/29/2005**Inventors:** Shaun Kazuo Wakumoto, Ballard Claude Bare, Cetin Ersoy**Title:** Load balancing with mesh tagging**Assignment: 1****Reel/Frame:** 015161 /
0453**Received:**
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5**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).**Assignors:** WAKUMOTO, SHAUN KAZUO**Exec Dt:** 03/23/2004BARE, BALLARD CLAUDE**Exec Dt:** 03/26/2004ERSOY, CETIN**Exec Dt:** 03/26/2004**Assignee:** HEWLETT-PACKARD DEVELOPMENT COMPANY, L.P.

20555 SH 249

HOUSTON, TEXAS 77070

Correspondent: HEWLETT-PACKARD COMPANY

JAMES K. OKAMOTO

INTELLECTUAL PROPERTY ADMINISTRATION

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